

**\*\*\*SPECIAL OPTION\*\*\***

Model 3360-S001

Dual Pulse Train Generator

April, 1988

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Lockport, Illinois

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Model 3360  
(Rev. 4/18/88)

**\*\*\*SPECIAL OPTION\*\*\***

The module 3360-S001 is the same as the module 3360-PLA except that the clock has been fixed at 1 MHz with 50% duty cycle. The capability of external control of the clock has been removed.

Output transformers have been removed and replaced with TTL level signals.

MLH:rem(3000 Ser. 13)  
Rev. April 18, 1988

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## Dual Pulse Train Generator

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(Rev. Feb. 87)

### FEATURES

- Two independent channels
- Two outputs per channel
- Transformer coupled outputs
- Internal clocks
- Programmable number of pulses
- 32,768 maximum count

### APPLICATIONS

- Drive x-y incremental plotter
- Drive two stepping motor translators
- Generate test pulse trains

### GENERAL DESCRIPTION

The Model 3360 is a single-width CAMAC module containing two separate channels for producing trains of output pulses. Writing a 16-bit sign and magnitude word into a countdown register initiates a process that counts the register down to zero while generating an output pulse at each count. This module can be used to drive two stepping motors, an x-y plotter, or any similar two-coordinate device.

Two transformer-coupled outputs are provided for each pulse train; the pulse train is steered to one or the other by the sign bit. The countdown is halted at a count of zero or upon ground closure of one of two external inputs. The sign bit determines which external input is able to inhibit the counting process, and an attempt will fail to restart the counting by writing a signed number while the related input is grounded. The number will not be written, and  $Q=0$  will be returned. The countdown registers can be read and written.

A separate clock is provided for each channel, and pulse rates are adjustable from 50 to 400 pulses per second. Nominal pulse width is 70 microseconds. Means are provided to allow external control of the pulse rates.

A LAM source, provided for each channel, is cleared by a Write command and is set by the count reaching zero or stopping due to external inhibit. The LAM sources may be tested, or the 8-bit status register, which contains LAM source information, may be read. The LAM sources may be separately enabled and disabled.

The 3360 is used primarily as a dual-channel stepping motor controller. The pulses for clockwise (CW) rotation are available at one output and for counterclockwise (CCW) rotation at the other. A "1" in the sign bit designates CCW rotation. CW and CCW limit switches can be ground-connected to the external inputs. Operation of the CW limit switch, for example, will inhibit a CW pulse train, will not inhibit a CCW pulse train, and will cause any Write commands with 0 in the sign bit to fail.

A transistor output for each channel drives a relay controlling power to the motor. An 8-bit status register may be read (four bits per channel) to ascertain the motor status. The four bits are LAM source, CCW limit, CW limit, and motor power.

On power-up, the outputs are inhibited, and the LAM sources are set.

3360  
PULSE  
GEN

○ N  
○ LE1  
○ LS1  
○ CWL1  
○ CCWL1  
○ LE2  
○ LS2  
○ CWL2  
○ CCWL2



FRONT PANEL

LED's

- N - Flashes whenever this module is addressed.
- LE - Indicates that the LAM request is enabled.
- LS - Indicates that the LAM source is set.
- CWL - Indicates that the clockwise limit is present.
- CCWL - Indicates that the counter-clockwise limit is present.

EXTERNAL CONTROL SIGNALS

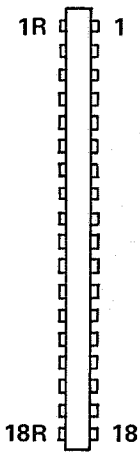
The rate or clock can be controlled by external signals. Use of the external clock requires removal of strap 1 PR. A high to low transition of the external clock signal constitutes one tick of the clock. Use of the external rate control also requires removal of strap 1 PR. The rate can be controlled by a 0 to 24 volt signal.

OUTPUT SIGNALS

The pulse rate is set at 100 pps at the factory. The pulse output is five volts into 390 ohms, transformer coupled.

STATUS REGISTER

Bit	True State Definition
1	Lam Source Set 1
2	Counter Clockwise Limit 1
3	Clockwise Limit 1
4	Motor 1 Power On
5	Lam Source Set 2
6	Counter Clockwise Limit 2
7	Clockwise Limit 2
8	Motor 2 Power On



FACE VIEW

Pin/Wire List

18/36 POSITION P.C. EDGE

<u>PIN NO.</u>	
1R	Chassis Common
2R	CH-1, CW pulse out
3R	CH-1, CCW pulse out
4R	CH-2, CW pulse out
5R	CH-2, CCW pulse out
6R	
7R	
8R	
9R	CH-1, external clock
10R	CH-1, external rate control
11R	CH-2, external clock
12R	CH-2, External rate control
13R	CH-2, control out
14R	CH-1, control out
15R	CH-1, CW limit in
16R	CH-1, CCW limit in
17R	CH-2, CW limit in
18R	CH-2, CCW limit in

<u>PIN NO.</u>	
1	Chassis Common
2	
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	Chassis Common
16	
17	
18	Chassis Common

Pulse rates factory set for 100 pps.

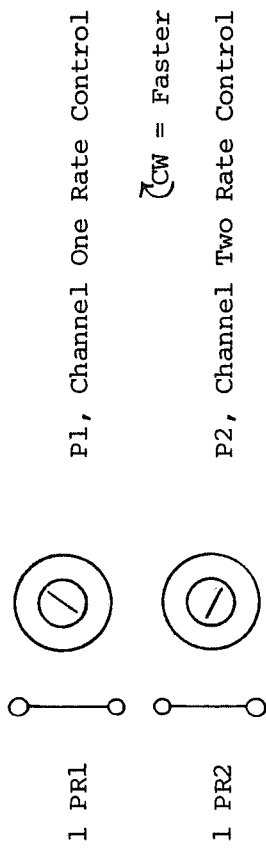


Figure 1 --- Straps and Adjustments